



تقدم لجنة EICoM الاكاديمية

تلخيص لمادة:

# مختبر لغة تجميع ومعالجة دقيقة

جزيل الشكر للطالبة:

## مروة حلوة







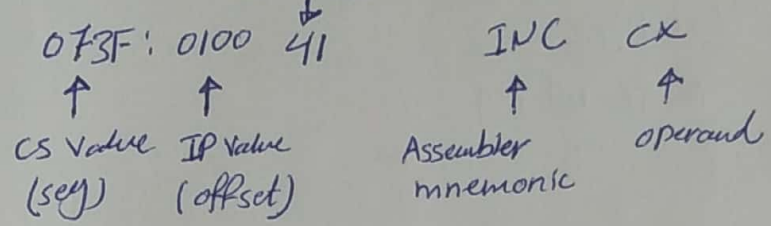
3

4) RF [Press enter] ZR  $\rightarrow$  to change any single flag  
 $\rightarrow$  display the flags.

Flag mnemonics :- (orderd).

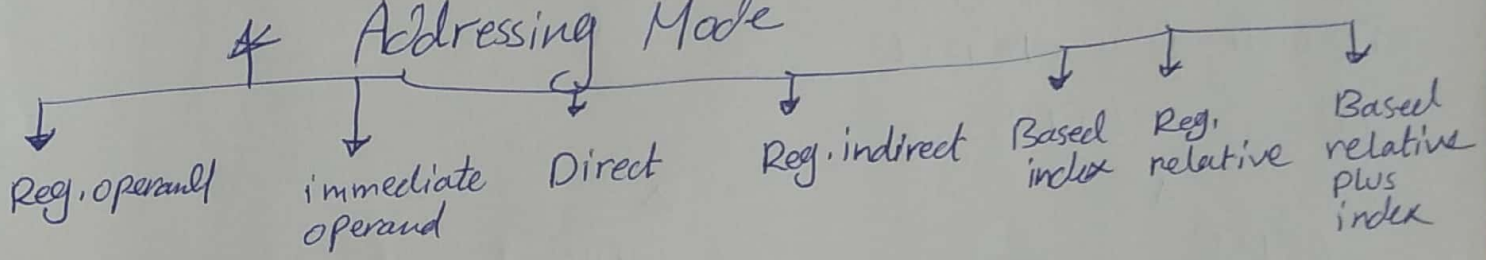
set	OV	DN	EI	NG	ZR	AC	PO	CY
or clear	NV	UP	DI	PL	NZ	NA	PE	NC

R command  $\Rightarrow$  display next inst machine inst



5) Q (Quit): quit debug & return to DOS.

### \* Addressing Mode



### \* Instruction Set :-

1) Arithmetic Instruction:

- 1) Add op1, op2  $\Rightarrow$  op1 = op1 + op2
- 2) sub op1, op2  $\Rightarrow$  op1 = op1 - op2 word
- 3) MUL operand  $\Rightarrow$  Ax = AL \* OP / (DX AX) = Ax \* OP  
Reg  $\leftarrow$  mem
- 4) Div operand  $\Rightarrow$  AL = Ax / OP word  
 AH = remainder DX = remainder  
(word / byte)

## ② Data transfer Instructions :

### ① General purpose instructions :

MOV: copy data from source to dest

MOV D, S      (S) → (D)

### ② Stack Instructions :

① Push: write word into stack

Push (16-bit reg) ⇒ ① SP = SP - 2

② 16-bit reg → SS:SP

② Pop: read word from stack to dest. reg.

POP (16-bit reg) ⇒ ① SS:SP → 16-bit reg.

② SP = SP + 2.

### ③ string instructions: depend on direction flag.

CLD instruction ⇒ D = 0

STD instruction ⇒ D = 1

①

LODS

LODB

LODW

DF = 0

DF = 1

DF = 0

DF = 1

AL = DS:[ESI]  
SI = SI + 1

AL = DS:[SI]  
SI = SI - 1

AL = DS:[ESI]  
SI = SI + 2

AL = DS:[ESI]  
SI = SI - 2

②

STOS

STOSB

STOSW

DF = 0

DF = 1

DF = 0

DF = 1

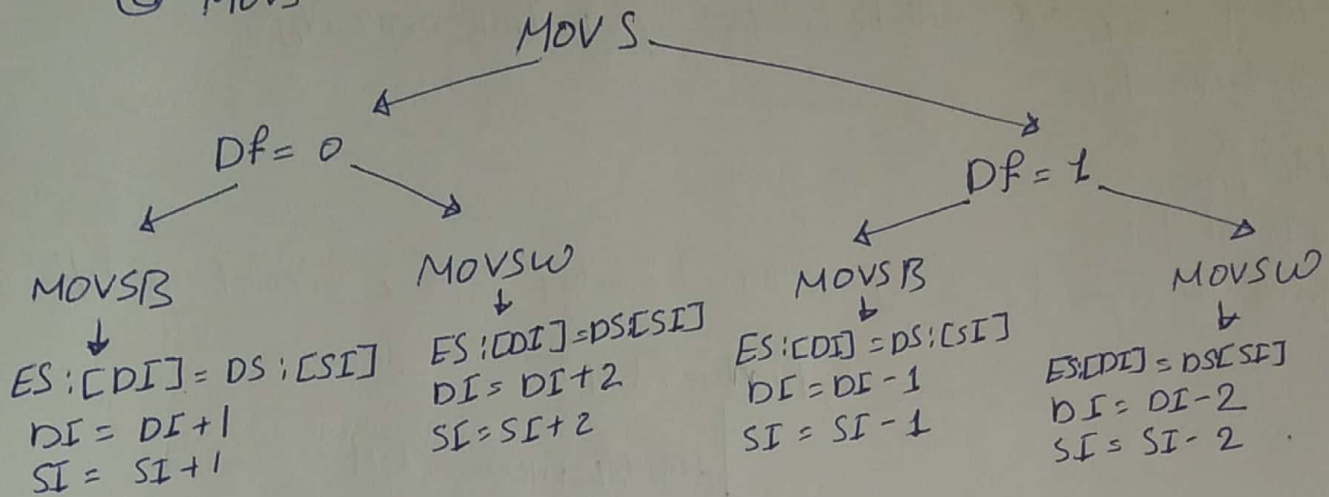
ES:[EDI] = AL  
DI = DI + 1

ES:[EDI] = AL  
DI = DI - 1

ES:[EDI] = AL  
ES:[EDI+1] = AH  
DI = DI + 2

ES:[EDI] = AL  
ES:[EDI+1] = AH  
DI = DI - 2

### ③ MOV S



④ REPEAT (REP) : repeat instruction(s) until the contents of register CX become equal zero, useful in: MOV S & STOS

examples in manual

exp 2 : Developing Assembly language & executing using Emu & MASM

\* MASM :-

① Assemble the source file (.ASM file) .

open DOSbox → @ to D:/8086 → MASM /L exp2.asm

\* (Assemble the source file into a machine language object file)

\* (MASM checks the source file for syntax errors)

② object file (.obj) .

\* before feeding the .obj file into linker, all syntax errors must be corrected .

③ list file (.lst)

\* optional file

\* very useful for programmers? it lists all the opcodes & offset add & errors that MASM detected .

④ executable file & MAP file (.exe & .map)

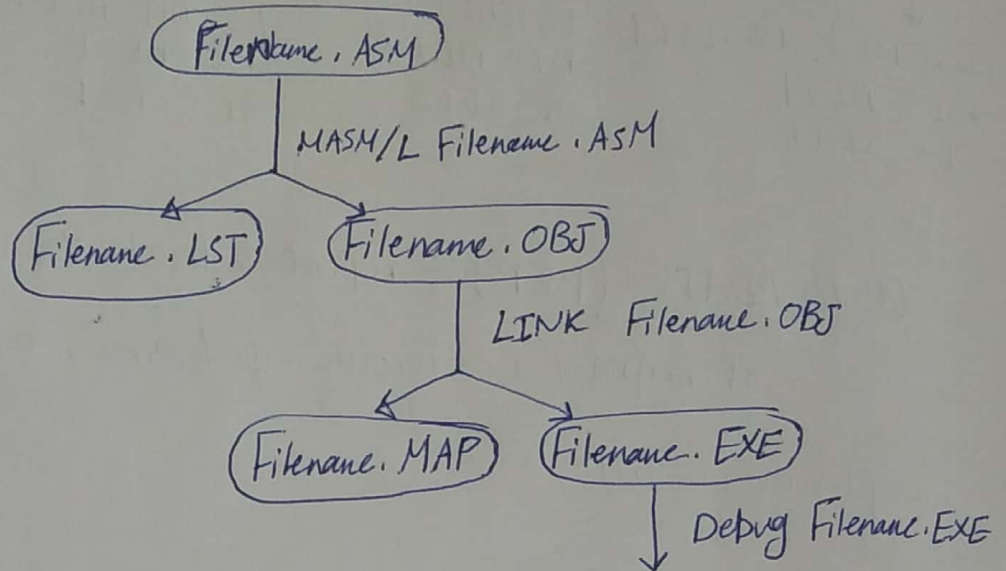
\* .exe file & .map file will be created by  $\Rightarrow$  Link exp2.obj

① Executable file

② MAP file

\* optional file

\* gives the name of each seg, where it starts & stops, size of seg in bytes



## \* Developing Assembly Language Program

① Data definition:

Sizes (DB, DW, DD, ...)

**error** the value assigned is greater than memory size allocated in to

Illegal inst.

① seg to seg data transfer `MOV DS, ES` / `MOV DS, CS` .. illegal

② code seg (CS) cannot be a dest op. `MOV CS, Ax` illegal

③ write an immediate value to seg `MOV ES, 1422h` illegal

④ Inst that have operands with diff. sizes `VAR DB 5`  
`MOV Ax, VAR` error

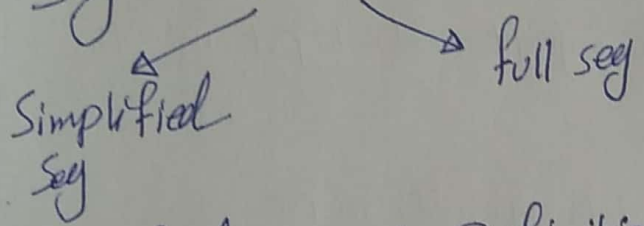
# Directives

# Instructions

- ① use to tell assembler what to do
- ② Commands that are recognized & acted upon by the assembler
  - Not part of inst. set
  - Used to
    - ① declare code
    - ② Data areas
    - ③ select memory model
    - ④ declare procedures
- ③ Different assemblers have different directives.  
NASM not equal MASM

- ① Use to tell CPU what to do
- ② Assembled into machine code by assembler.
- ③ Executed at runtime by CPU
- ④ Member of Intel IA-32, IA-16 inst. set

## \* Segment definition :



### ① Simplified segment Definition

- Model memory model
- Data ; ← values default → decimal
- Code
- startup [ ]
- Exit
- END

### ② Full segment Definition

```

Name1 Segment
; ;stack
Name1 ENDS

Name2 Segment
; ;Data
Name2 ENDS

Name3 segment ;code
ASSUME CS: Name3, DS: Name2, SS: Name1
MOV AX, MyData
MOV DS, AX
;
Name3 ENDS
  
```



Notes:-

① Write number in hexa  $\rightarrow$  AAMX  $\rightarrow$  OAAH  $\checkmark$   
 اذا كان الرقم يبدأ بـ F-A لازم اكتبه بـ حرفين

② MOV CS, 500  $\rightarrow$  هاي فين يا غلبين

① CS  $\rightarrow$  dest op ما بيكون

② ~~reg~~ reg رقم ر

memory model table  
\* \* \*

③ lea ~~SI~~, ~~SI~~ Ax  $\equiv$  mov ~~SI~~, offset ~~SI~~ Ax

④ examples of Directives  $\rightarrow$  .Model<sup>①</sup> / .Startup<sup>②</sup> / .Exit<sup>③</sup> / .Data<sup>④</sup> / .Code<sup>⑤</sup>  
 Segment<sup>⑥</sup> / ENDS<sup>⑦</sup> / ASSUME<sup>⑧</sup>

⑤ you can define var. in code fstack seg. but it's more correct programming practice to keep them in data segment

⑥ we have two problems:

① the assembler needs to know which segment addr. to put into the data segment register (DS).

SOL MOV Ax, MyData; setup our own data seg. addr. in DS

MOV DS, Ax ; can't load seg. reg. directly from memory

which

② Form of memory-addressing machine instructions to use the assembler doesn't know that this move took place.

SOL the ASSUME directive

# Problema

# Sum & average code

ex1

```

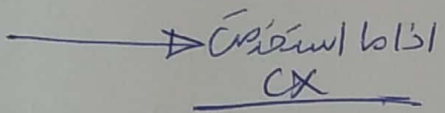
Model small
data
V3 DB 1,2,3,4,5
code
startup
mov SI, offset V3
mov AL, 00
mov CX, 5

```

```

L1: Add AL, [ESI]
    inc SI
Loop L1

```



```

L1: Add AL, [ESI]
    inc SI
    dec CX
    cmp CX, 0
    jne L1
} ≡ Loop

```

End

ex2

```

seg1 Segment
V4 dw 2,3,4,5, 0AAH, 0FFH, 20H.
Avg DB ?
Sum dw ?

```

seg1 ends

seg2 Segment

```

Assume CS: seg2, DS: seg1
mov Ax, offset seg1
mov DS, Ax
mov SI, offset V4
mov Ax, 00
mov CX, 7

```

```

L1: Add Ax, [ESI]
    Add SI, 2

```

Loop L1

```

mov Sum, Ax
mov BL, 7

```

Div BL ; remainder AH & result AL

```

mov Avg, AL

```



## 11 Subroutine

- ① Perform one task
- ② CALL & RET instructions
- ③ accessed via CALL inst.
- ④ slower, because of call & ret inst
- ⑤ Run

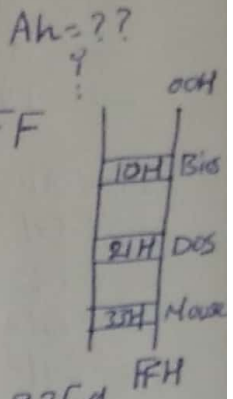
## Macro

- ① Perform one task
- ② MACRO & ENDM directives
- ③ inserted in the program at the point of usage
- ④ Faster, placed macro in the program by assembler at the point they are invoked
- ⑤ Assemble

## exp 4: BIOS Interrupts Programming

\* INT xx ; the interrupt number xx can be 00-FF

- ① types 0-7  $\Rightarrow$  reserved by Intel  
0-4  $\Rightarrow$  being predefined  
5  $\Rightarrow$  IBM uses for print screen  
6 & 7  $\Rightarrow$  not used.
- ② types 8-Fh  $\Rightarrow$  generated by hardware devices connected to 8259.
- ③ types 10h-1Fh  $\Rightarrow$  can be called by app programs to perform various I/O operations & status checking.



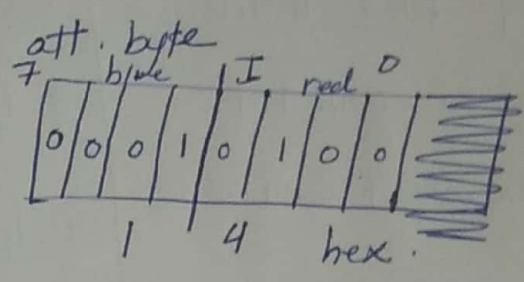
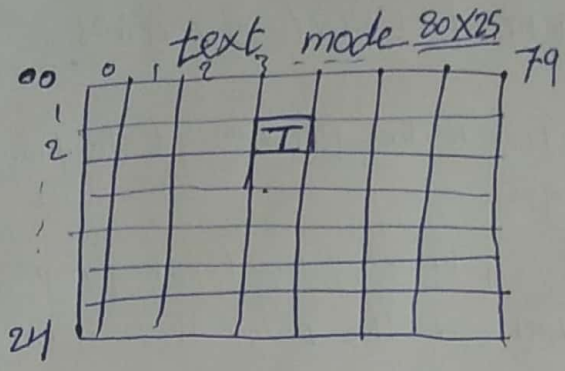
## Text display

\* Display mode:  
 $\rightarrow$  text mode  $\rightarrow$  display char.  
 $\rightarrow$  Graphics mode  $\rightarrow$  display pixels.

\* video adapters:  
 $\rightarrow$  MDA  $\rightarrow$  only display text  
 $\rightarrow$  CGA  $\rightarrow$  display in color both text & graphics.

\* Mode number: \* largest is column number.

\* attribute byte :- the high byte of the word that specifies a display character,



فأية 0 ← I → 1

text mode ⇒ default select display mode.

graphic mode ⇒ must to select display mode.

- model small
- code

```

mov AH, 0
mov AL, 3
int 10H
    
```

} ① display mode

```

mov AH, 2
mov DH, 2
mov DL, 3
mov BH, 0
int 10H
    
```

} ② move cursor

```

mov AH, 9
mov AL, 'I'
mov CX, 1
mov BL, 14H
int 10H
    
```

} ③ display char

fun: 9 & Ah & Eh

Display char

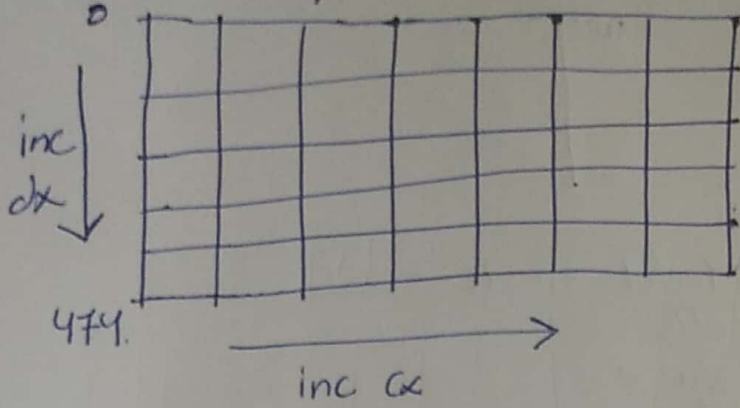
- 9 → cannot move cursor
- Ah → ما بين آتري
- Eh → ما بين اللون / ما بين آتري

end

## \* Graphics Modes

- \* Pixel: picture element
- \* APA: all points addressable
- \* number of columns & rows gives the resolution of the graphic mode.

Graphics mode



12H  
640 x 480  
columns x rows

الصفحة \*  
العمود \*  
السطر \*

• Model small

• Code

```
mov Ah, 0
mov AL, 12H
int 10H
```

} select mode

```
data
str1 db "Micro..."
code
mov SI, offset str1
mov AL, [SI]
inc SI
```

} display

```
mov AH, 0CH
mov AL, 0AH
mov Cx, 100
mov Dx, 100
mov BH, 0
int 10H
```

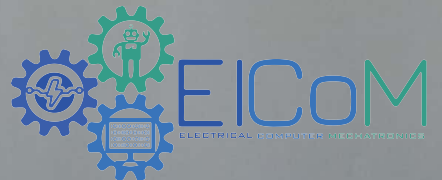
} write pixel

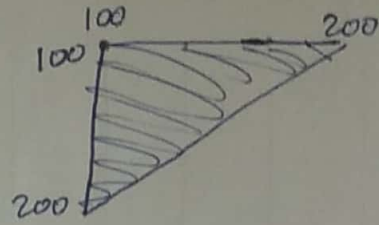
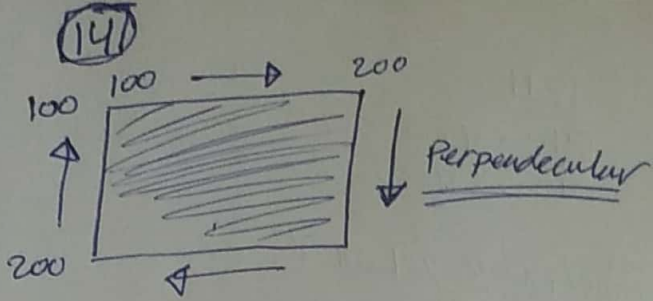
```
mov AH, 0CH
mov AL, 0AH
mov Cx, 100
mov Dx, 100
mov BH, 0
L1: int 10H
inc Cx
cmp Cx, 300
jne L1
```

} draw horizontal Line

```
mov AL, 0CH
mov Cx, 300
mov Dx, 100
mov AH, 04
L1: int 10H
inc dx
dec Cx
cmp dx, 300
jne L1
```

} draw Line





• Model small

• data

~~• data~~

• code

```
mov Ah, 0
mov AL, 12H
int 10H
mov Ah, 0CH
mov AL, 0AH
```

```
mov Cx, 100
mov Dx, 100
```

```
L1: int 10H
inc Cx
cmp Cx, 200
jne L1
```

```
inc Dx
mov Cx, 100
cmp Dx, 200
jne L1
```

• Model small

• data

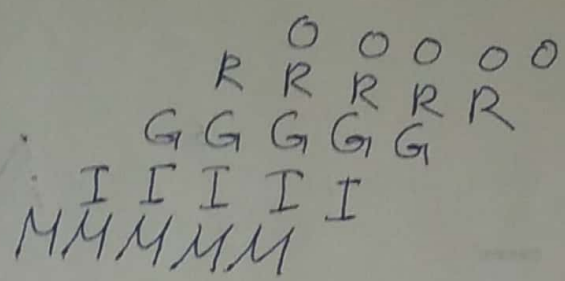
V1 dw 0

• code

```
mov Ah, 0
mov AL, 12H
int 10H
mov Ah, 0CH
mov AL, 0AH
mov V1, 200
mov Cx, 100
mov Dx, 100
```

```
L1: int 10H
inc Cx
cmp Cx, V1
jne L1
dec V1
inc Dx
mov Cx, 100
cmp Dx, 200
jne L1
```

Q1 display



· model small

· data

```

v1 db 0
x1 db 20
x2 db 3
  
```

· code

```

· startup
mov ah, 0
mov al, 3
int 10h
  
```

```

M1 macro v1
mov ah, 2
mov dh, x1
mov dl, x2
mov bh, 0
int 10h
mov ah, 0ah
mov bh, 0
mov al, v1
mov cx, 5
int 10h
M1 endm
  
```

```

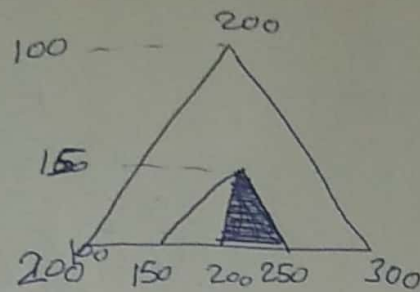
mov v1, 'M'
M1 v1
dec x1
inc x2
mov v1, 'I'
M1 v1
dec x1
inc x2
mov v1, 'G'
M1 v1
dec x1
inc x2
mov v1
mov v1, 'R'
M1 v1
dec x1
inc x2
mov v1, 'O'
M1 v1
ret
  
```



Q2

Model small  
data v1, dwo  
code  
mov Ah, 0  
mov AL, 12H  
int 10H

mov Ah, 0CH  
mov AL, 0AH  
~~int 10H~~



```
mov cx, 200
mov dx, 100
L1: int 10H
    inc dx
    dec cx
    cmp dx, 200
jne L1
```

```
mov cx, 200
mov dx, 100
L2: int 10H
    inc dx
    inc cx
    cmp dx, 200
jne L2
```

```
mov cx, 100
mov dx, 200
L3: int 10H
    inc cx
    cmp cx, 300
jne L3
```

```
mov dx, 150
mov cx, 200
cmp dx, 200
jne L4
```

```
L4: int 10H
    inc dx
    dec cx
    cmp dx, 200
jne L4
```

```
mov dx, 150
mov cx, 200
mov v1, 200
L5: int 10H
    inc cx
    cmp cx, v1
jne L5
    inc v1
    inc dx
    mov cx, 200
    cmp dx, 200
jne L5
```

①  
exp 5

# DOS Mouse Interrupts Programming :-

DOS → 21H  
mouse → 33H } AH → function number

output in AL

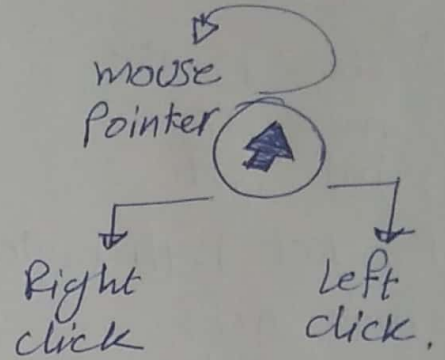
## \* Function 9:

Print a string:

- $\$$  →  $\text{string}$
- DS →  $\text{segment}$

ex

```
.data  
str db 'My name is Marwa$'  
.code  
start up  
mov Ah, 9  
mov Dx, offset str  
int 21H.
```



## \* Function 2:

When DL = A ⇒ Linefeed ⇒ display result will be

A
B
C
!

سطر و فراغ

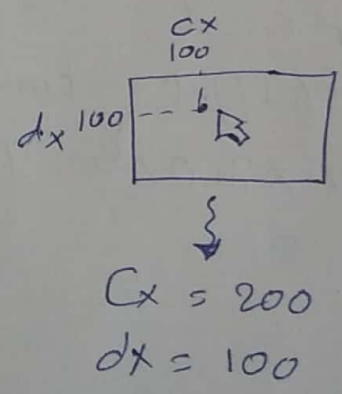
## \* Function 00h:

•  $\text{mouse}$  →  $\text{interrupt}$  →  $\text{mouse}$

MOUSE →  $\text{mouse}$  \*  
①  $\text{Graphic mode}$  على  $\text{mouse}$   
②  $\text{mouse}$  →  $\text{fun}$  →  $\text{mouse}$  →  $\text{mouse}$   
•  $\text{mouse}$

## \* Function 03h:

\* the value of CX is doubled



②

① select mode (Graphic)

```
Mov AH, 0  
MOV AL, 13H  
Int 10H.
```

② mouse initial

```
MOV AX, 00  
int 33H
```

③ show pointer

```
mov AX, 1  
int 33H.
```

④ select both buttons.

```
mov AX, 3  
int 33H.
```

⑤ work when click.

```
Cmp Bx, 1  
JE left  
Cmp Bx, 2  
JE right.
```

⑥ infinit loop for all of this (4 & 5.)

AGAIN;

!

JMP AGAIN

⑦ move cursure :-

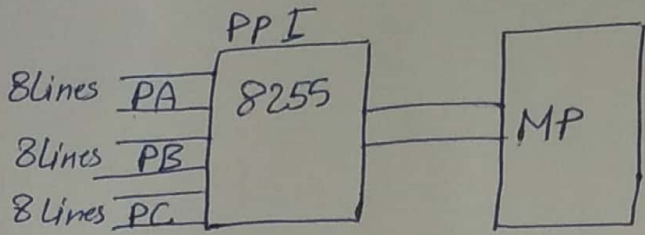
① ~~shift right~~ Cx (doubled value) .  
shr cx, 1

② shift 6 right 3 times where we write a ~~char~~ char

③ ~~13H~~ 320 x 200  $\xrightarrow{8}$  40 x 25

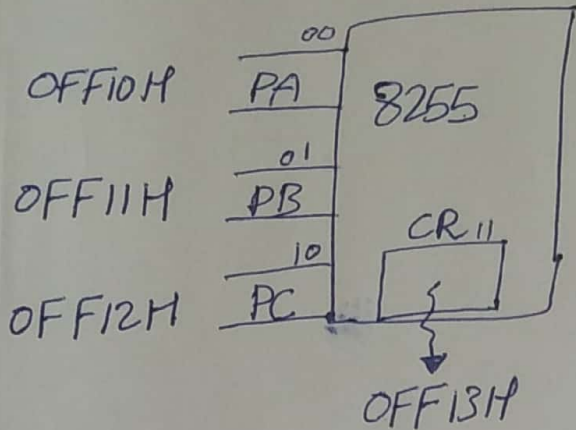
(4)

### EXP 6 Parallel data In/out



\* address

16 line → each digit tack 4 lines



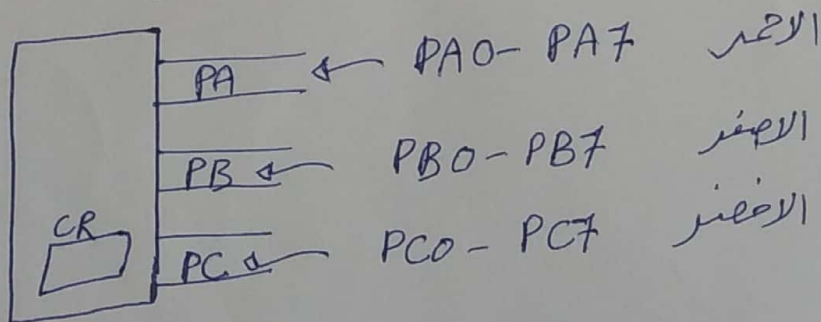
1111	1111	0001	0000	← OFF10H
1111	1111	0001	0001	← OFF11H
1111	1111	0001	0010	← OFF12H
1111	1111	0001	0011	← OFF13H

ex if addresses of ports is OFF1FH, OFF5FH, OFF9FH, OFFDFH.  
Find A1 & A0

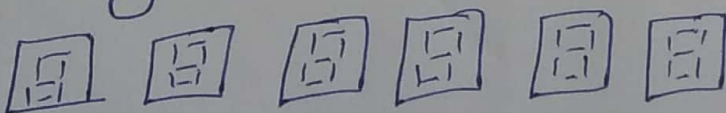
1111	1111	0001	1111
1111	1111	0101	1111
1111	1111	1001	1111
1111	1111	1101	1111

IDE Cople 25 Pin

\* traffic light :



\* Seven segments



dot matrix

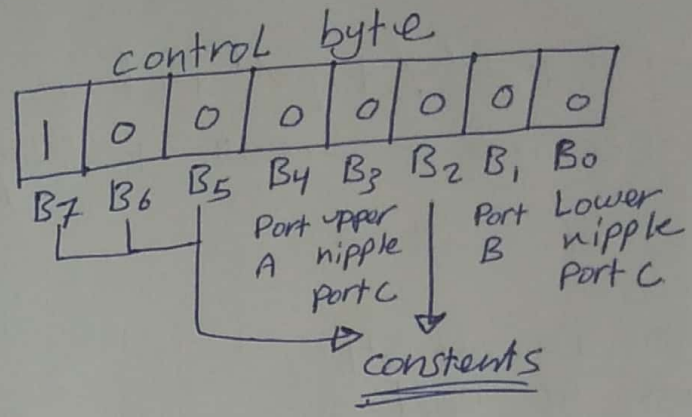


⑤ turn on red leds :-

```

• model small
• code
mov dx, 0ff13h
mov AL, 80h
out dx, AL
    
```

address 13, 000



```

mov dx, AL. X
    ↑
    syntax error.
    
```

```

mov Dx, 0ff10h } ALL
mov AL, 0ffh   } LEDS
out Dx, AL     } ON
    
```

Put: 0 → output  
1 → input

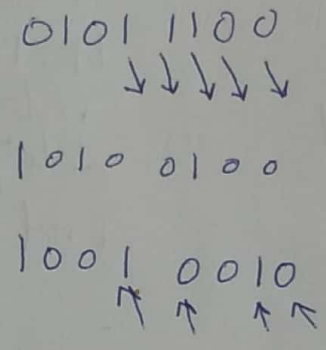
```

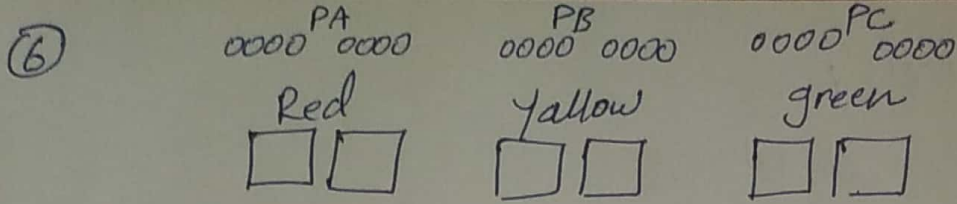
mov Dx, 0ff10h } واحد
mov AL, 55h     } ON
out dx, AL     } وواحد
end            } off
    
```

Port C → الوعيد اللي مسكن  
ينقسم لجزئين

```

mov Dx, 0ff10h }
mov AL, 0ffh   } جميع
out Dx, AL     } الالوان
inc Dx         }
out Dx, AL     } OR.
inc Dx         }
out Dx, AL     }
end            }
    
```





4MHz  
speed  
Need delay

+ jeq AL, 0 + delay + port Red جاءت ①  
- jeq AL, 0 + delay

- model small
- code

```
mov Dx, off13H
mov AL, 80H
out Dx, AL
```

Port A Again:

```
mov Dx, off10H
mov AL, offH
out Dx, AL
```

6 delay

call delay →

```
mov AL, 00
out Dx, AL
```

Port B mov Dx, off11H.

```
mov AL, offH
out Dx, AL
```

call delay

```
mov AL, 00
out Dx, AL
```

Port C mov Dx, off12H

```
mov AL, offH
out Dx, AL
```

call delay

jmp Again.

delay proc

```
mov cx, 0ffffH
```

L1: loop L1

ret

delay endp

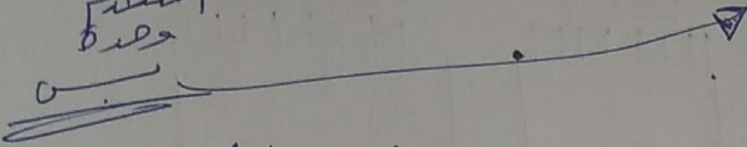
End.

---

```
loop L1 ≡ dec cx
           cmp cx, 0
           jne L1
           ret
```

(7) tern on seven segment :

5<sub>v</sub> counter on port B, 0 → F, 00 → FF



- model small
- code

```
mov Dx, 0FF13H
mov AL, 80H
out Dx, AL
```

Again:

```
mov Dx, 0FF11H
mov AL, 00
```

```
L1: out Dx, AL
```

```
call delay
```

```
inc AL
```

```
cmp AL, 10H → مساكن تبين الـ F
```

```
jne L1
```

```
jmp Again.
```

Notes:

① down counter ⇐ dec ⇐ inc

② odd → inc AL  
cmp AL, 11H

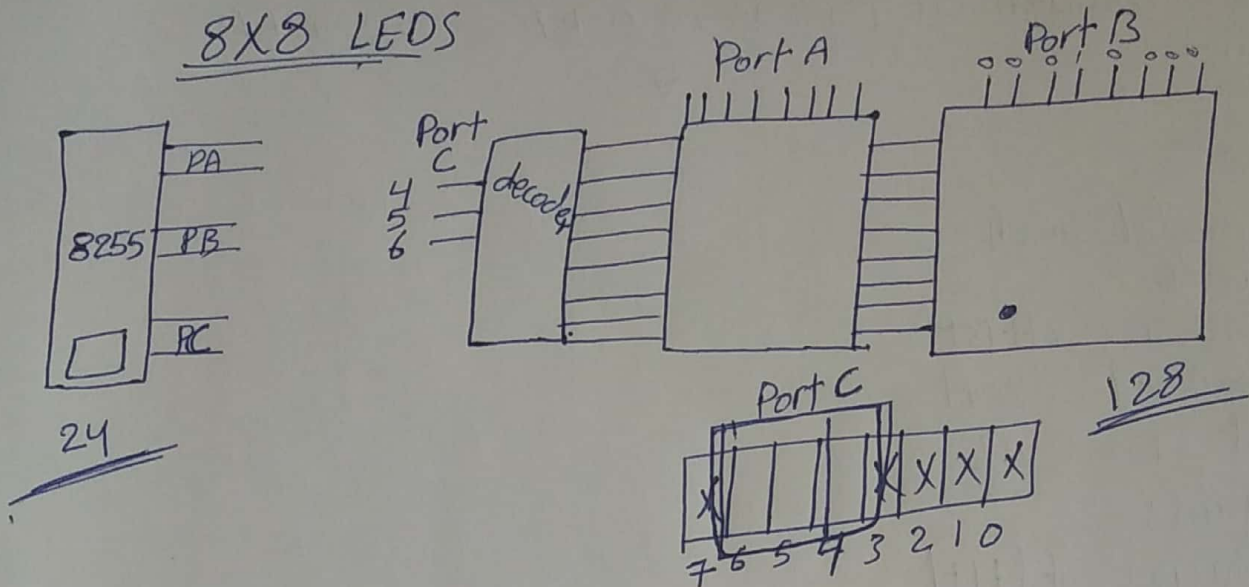
③ even → inc AL  
cmp AL, 10H.

الـ FF ≠ zero قبل الـ



# exp 7 I/O Apps: Dynamic Display.

## 8X8 LEDS



\* display specific dot.

• code

```

mov Dx, 0FF13H } initialization.
mov AL, 80H
out Dx, AL

```

```

mov Dx, 0FF11H } Port B
mov AL, 10H      } 0001 0000
out Dx, AL

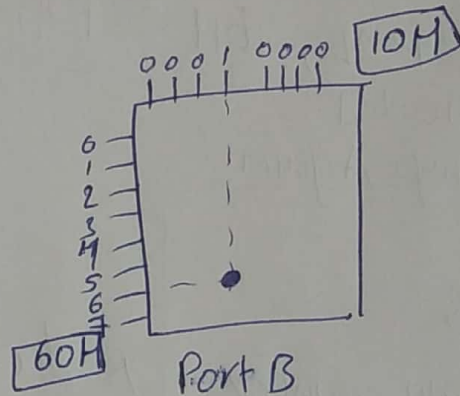
```

```

mov Dx, 0FF12H } row 7
mov AL, 60H    } port C -> decoder
out Dx, AL

```

End



```


* mov Dx, 0FF11H
  mov AL, 08H
  out Dx, AL
  mov Dx, 0FF12H


```

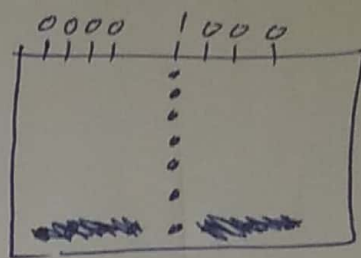
9

```

mov Dx, 0FF11H
mov AL, 08H
out Dx, AL

```

draw  
column



```

mov Dx, 0FF12H

```

```

Again: mov AL, 00

```

```

LI: out Dx, AL

```

```

Add AL, 10H

```

```

cmp AL, 80H

```

```

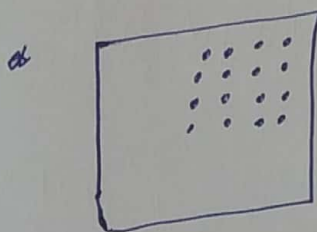
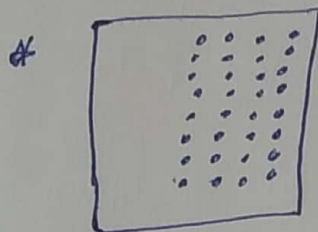
jne LI

```

```

jmp Again

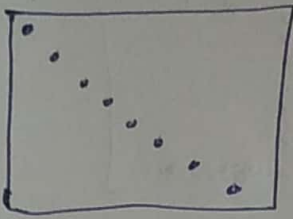
```



نفسو بين  
 mov al, 0F

نفسو بين  
 cmp AL, 40  
 +

10



• code

```

mov Dx, 0FF13H
mov AL, 80H
out Dx, AL
L2: mov BH, 80
    mov BL, 0
L1: mov Dx, 0FF11H
    mov AL, BH
    out Dx, AL
    shr BH, 1
    mov Dx, 0FF12H
    mov AL, BL
    out Dx, AL
    add BL, 10H
    cmp BL, 80H
    jne L1
    jmp L2

```

\* WORK ON computer

عمل البرنامج على الكمبيوتر

① ml Test.asm

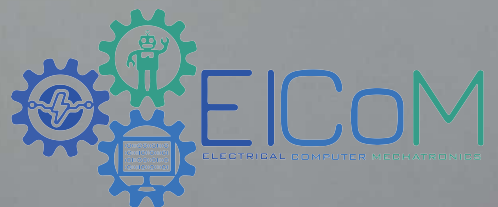
↓  
EXE file

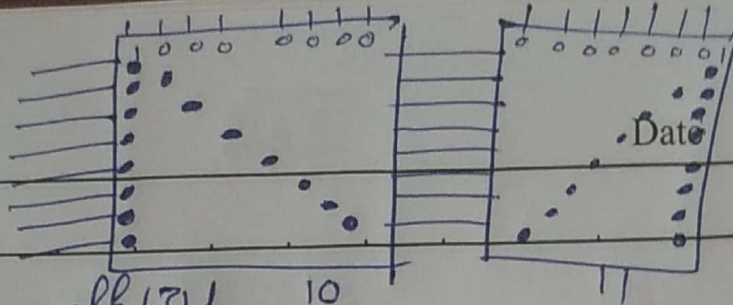
② exe2bin Test.exe

③ L 400 CR

④ g = 400

} كل  
البيانات





```
mov dx, off 13H
```

```
mov AL, 80H
```

```
out dx, AL
```

```
L2: mov bh, 80
```

```
mov bl, 0
```

```
mov ch, 01
```

```
mov cl, 0
```

```
L1: mov dx, off 10h
```

```
mov AL, bh
```

```
out dx, AL
```

```
shr bh, 1
```

```
mov dx, off 11h
```

```
mov al, ch
```

```
out dx, al
```

```
shl ch, 1
```

```
mov dx, off 12H
```

```
mov AL, bl
```

```
out dx, AL
```

```
mov AL, cl
```

```
out dx, AL
```

```
add bh, 10H
```

```
cmp bh, 80H
```

```
add cl, 10H
```

```
cmp cl, 80H
```

```
jne L1
```

```
mov dx, off 10h
```

```
mov AL, 80H
```

```
out dx, AL
```

```
mov dx, off 12H
```

```
mov AL, 00
```

```
L3: out dx, al
```

```
add al, 10H
```

```
cmp al, 80H
```

```
jne L3
```

```
mov dx, off 11h
```

```
mov al, 01h
```

```
out dx, al
```

```
mov dx, off 12H
```

```
mov al, 00
```

```
L5: out dx, al
```

```
add al, 10H
```

```
cmp al, 80H
```

```
jne L5
```

```
jmp L2
```

```
end
```

## Extra Code :-

### Counters :-

① counter 00 → OF (normal)

- model small
- code

```
mov dx, 0FF13H
mov AL, 80H
out dx, AL
```

Again: mov dx, 0FF11H → port B

```
mov AL, 00
```

```
L1: out dx, AL
```

```
call delay
```

```
inc AL
```

```
cmp AL, 10H → 0F + 01 = 10H
```

```
jne L1
```

```
jmp again
```

② odd counter 01 → OF

```
mov AL, 01
```

```
L1: out dx, AL
```

```
call delay
```

```
inc AL
```

```
inc AL
```

```
cmp AL, 11H
```

③ odd counter OF → 01

```
mov AL, 0FH
```

```
L1: out dx, AL
```

```
call delay
```

```
dec AL
```

```
dec AL
```

```
cmp AL, 00H
```

④ even counter 00 → OF

```
mov AL, 00
```

```
L1: out dx, AL
```

```
call delay
```

```
inc AL
```

```
inc AL
```

```
cmp AL, 10H
```

⑤ even counter OF → 00

```
mov AL, 0EH
```

```
L1: out dx, AL
```

```
call delay
```

```
dec AL
```

```
dec AL
```

```
cmp AL, FFH
```

and soon .